

GÖPEL electronic GmbH. Constantly growing, yet close to its customers.

Testing technologies that are a pleasure to use

GÖPEL electronic develops and manufactures electrical and optical measuring and testing technology, as well as test and inspection systems for electronic components, printed circuit boards, and also industrial electronics and automotive electronic systems. The company's business divisions are Automotive Test Solutions, Embedded JTAG Solutions, Industrial Function Test and Inspection Solutions (AOI, AXI, SPI, IVS). Besides the company's headquarters in the German city of Jena, GÖPEL electronic also operates multiple sales and service offices in the US, the UK, India and China. We also have numerous specialists involved in the global distribution and service network. For more information, visit www.goepel.com



Test solutions for the automotive industry. Test systems for automotive control units, bus communication and acoustic analysis as well as end-of-line applications



With its roots in optics. Established in electronics. Powerful testing and programming for the development and production of electronics

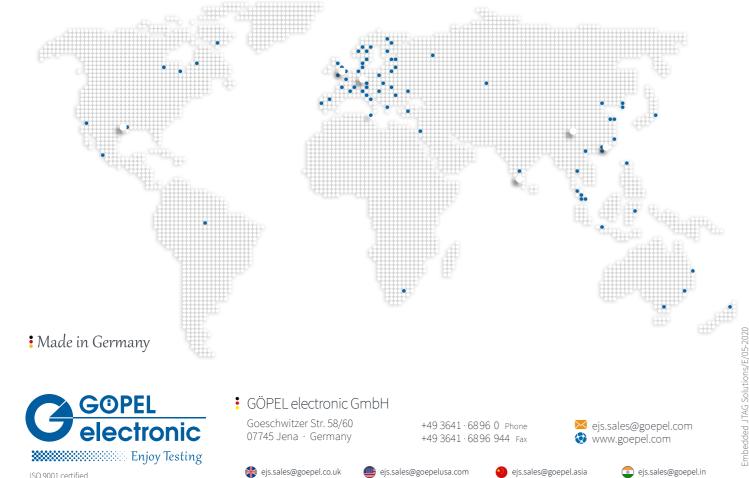


Tailor-made and customer-focused. Industrial Function est Customised test systems for industrial electronics and PLC systems

Inspection Solutions



Optics as an inspiration and benchmark. Inspection of printed circuit boards using Automated Optical Inspection (AOI), Solder Paste Inspection (SPI), Automated X-ray Inspection (AXI) and custom solutions (IVS)





Embedded JTAG Solutions Testing and programming every assembly



Do you need detailed error diagnostics?



Accelonix BV Luchthavenweg 18b • NL-5657 EB • Eindhoven • The Netherlands • : +31 40 750 1650 • E: info@acceld

ISO 9001 certified

Then Embedded JTAG Solutions are just what you're looking for!

Embedded Board Test Embedded Functional Test Embedded Programming

Areas of use

number of test points can be

omitted.

Embedded JTAG Solutions reveal the

test coverage that can be achieved right

from the first circuit diagram. As a result,

optimisations can be introduced as early

as the design process, and a large

Development, production, service and support

Penalis and Service

Popalita Service

Manufacturing

Manufacturing

Embedded JTAG Solutions

Since the introduction of the 1149.1 standard in 1990, GOEPEL electron has been working on hardware and software solutions that utilize the JTAG interface for testing board connections and functionality. Over the years, additional extensions to the standard and test technologies have been added, which are now grouped together under the term Embedded JTAG Solutions.

But what are these Embedded JTAG Solutions? Embedded JTAG Solutions consist of a total of three application areas.

Embedded Board Test

Embedded JTAG Solutions also provides Embedded Board Test for verification of functional board connections. With this capability, Boundary Scan, microcontroller and FPGA resources are used to find shorts, non-soldered pins and null resistor

and pull resistors.

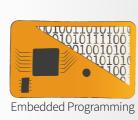
Embedded Functional Test

Embedded Functional Test

Today's test strategies now require more than just testing board connections. In addition to the traditional task of assuring perfect connectivity, Embedded JTAG Solutions provides Embedded Functional Test to also assure correct



Embedded Board Test





The growing demand for and challenges involved with in-system programming of a variety of data often represents a major hurdle today, especially with increasing file sizes and growing demands on programming speed. Embedded JTAG Solutions addresses such challenges by enabling the use of on-board resources to program at high speed.

Diagnostics on first prototypes can often prove difficult as there are no reliable testing options available for the hardware and software. Embedded JTAG Solutions make it possible to find or rule out any soldering and assembly defects, and all without any firmware or software on the board.

From individual assemblies to panel testing, Embedded JTAG Solutions enable a high level of test coverage in an extremely short test time, as well as allowing programming of assemblies in series production. This is possible both in panels and on all individual circuits in parallel.

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Embedded JTAG Solutions are also ideal for repair work. No matter whether faults appear during production or only after reaching the end customer – detailed error messages make repair work much easier.

> lt is often

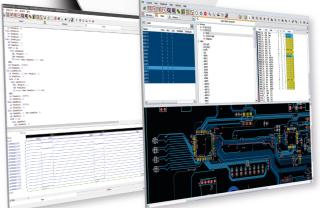
necessary to analyse malfunctions in detail either in the panel or separately. With Embedded JTAG Solutions, testing can be carried out as soon as a power supply and JTAG bus are available. A bed of nails is not required.

Development and prototyping

Embedded JTAG Solutions · Application



Development and prototyping



Production

Embedded JTAG Solutions · Application

Application · Embedded JTAG Solutions



Production





Repair · Analysis · Field

Embedded JTAG Solutions · Application

Application · Embedded JTAG Solutions

Faults

particularly annoying in high-quality assemblies. Yet faulty assemblies do not necessarily have to be destroyed.

> fast repair thanks to detailed fault analysis

fault prevention A precise fault analysis of the assemblies can preclude series faults and helps to prevent future defects.

Analysis and

Hardware debugging options without the need for

firmware

Solution

The Embedded JTAG Solutions also The Embedded JTAG Solutions also allows the repair of field returns. This plays a particularly important role in the automotive sector, for example, in the avoidance of product recalls. Such a test can also take place on site. The user therefore has the option to immediately repair the assembly or to replace it later.

Firmware update options for the software in the panel

> Cyclical panel testing also possible





Repair · Analysis · Field



Support

In terms of support you have around the clock access to the GENESIS user platform. Updates and upgrades for software applications as well as the latest product information such as training videos are available there. Furthermore, you receive worldwide comprehensive support for your special requirements thanks to a global partner network

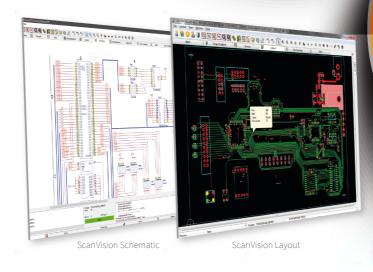
SCAN FLEX

11.00

Guidance

GOEPEL electronic accompanies and supports its customers right from the start. The Design-for-Testability Guide, for example, offers recommendations with which you can optimise later test processes as early on as possible. With the test coverage analysis for circuit diagram and layout you can optimise test points and test coverage.

Prototype testing from the very first assembly





Training

Customer requirements

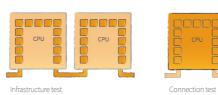
Be it system installation, on-site commissioning or in-line and standalone turnkey solutions: you can rely on the expertise of GOEPEL electronic employees at all times. Particularly with customer-specific adjustments, project creation or adapter planning and design, we are always on your side.

Training

Both as a user of and someone interested in our technologies, you can be brought right up to date at regularly occurring seminars, training sessions and webinars. At user conventions, such as the Boundary Scan Days, you can also expand your personal network and exchange experiences with colleagues. Interesting EMS service providers also have the option of becoming a partner in the "EMS programme" cooperation network. And, of course, the experts at GOEPEL electronic will help you to find the test strategy tailored to you.

Software

Structure tests, function/emulation tests, programming







Flash programming



Features that make all the difference



Simple, fast and targeted project development using intelligent tools and automated system processes



Support for testing and programming strategies that goes beyond boundary scanning, for internal and external instrumentation





Interactive visualisation at the layout, schematic and logic level for graphical analysis and debugging



Integrated protective functions block scan vectors that damage hardware, guaranteeing safe test programs



Scalable high-performance platform with over 50 integrated tools, a central project database and consistent user interface



Enhanced test coverage and precise error diagnostics



thanks to full inclusion of nonscan circuitry components

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● 110 ● 111

void CalcMEk1(float Eta, float Etb

a = 1/Eta;	nbda	lar	float	
/Etb;	= 1	mu	float	
ambda/mu;	= 1	ro	float	
= (float)k;	loat	kf	float	
		>1)	if (ro)	
- 1/			}	
float.PositiveInfi	= 2	ENS	m	
float.PositiveInfi	= M	_ENI	m	
float.PositiveInfi	= 2	ft	m	

nfi m_ftW = float.PositiveInfi return;

 $m_{fNS} = (ro / (1-ro)) * (1- (r))$ m_fNW = (lambda*lambda/(k*mu*m m_fts = m_fNS / lambda; m_ftV = ((kfloat+1) / (2*kfloa

double s = (double)Etb/Math.Sq double vb = (s*s)/(Etb*Etb); float v = 0.5f* (1+(float)vb);CalcPn(v, ro, m_aPN);

void CalcGG1(float Eta, float Vart

float lambda = 1/Eta; float mu = 1/Etb; float ro = lambda/mu; if(ro>1)

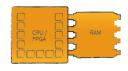
11 m_fNS = float.PositiveInfi m_fNW = float.PositiveInfi

Solution

The key for a successful use of the Embedded JTAG Solutions, more than ever before, lies in the quality of the software used. Various editions ensure that, depending on the requirements in development and production, the best possible service is available. As a pioneer of automatic test programme creation, GOEPEL electronic offers the complete range, from the smallest studio system to the high-end version. The modular expansion-capability, as well as licensing that can be adapted to every need, ensure a high degree of flexibility.

Software





Memory test



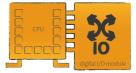


High-speed test via universal FPGA design

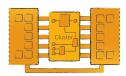




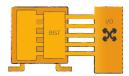
Microcontroller programming



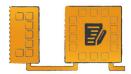
I/O connection test



Logic cluster test



Integrated self-testing



PLD/FPGA programming



Analogue I/O-Test

Hardware

Embedded JTAG Solutions

SCANFLEX controller for industrial use

\$ 1/0

I/O Modul

\$\$ 1/0

UUT

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Power Boot LAN



Scalable high-performance platform for scanning operations of 100 MHz, in parallel on up to eight independent TAP interfaces

Separately controlled I/O modules with VarioCore® technology for reconfigurable analogue, digital and mixed-signal functions

Flexibility

The best transmission quality for TAP signals, even over large distances of up to ten metres, with full runtime compensation

Data transmission quality



Controllers, I/O modules, TAP transceivers and TAP-interface cards that can be freely combined, enabling scalable system configurations

Specialist front-end hardware guarantees seamless

integration in in-circuit testers, flying probe testers, function

testers and other ATEs

Support for testing and

instrumentation

programming strategies that goes beyond boundary scanning, for internal and external

Modularity



ATE ready



Universality



I/O-module with 96 mixed-signal channels

Solution

Just as with software, hardware is also a part of the GOEPEL electronic tradition of fulfilling the highest quality and service demands throughout the entire product. In perfect teamwork with the software, the Embedded JTAG Solutions make applications possible that go far beyond the standard boundary scan. There is a particular focus on flexibility in the development of our hardware lines. This means all systems can be scaled and expanded

Controller / TAP Tranceiver 2 X TIC TIC TIC TIC

ChipVORX fixture module for testing high-speed modules



VPC-based TAP16

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Hardware



SCANFLEX controller for industrial use



USB/LAN controller for up to eight units under test



SCANFLEX II module for error-free (differential) data transmission



Transceiver interface card for integration in adapter solutions