



ATE System Integration

Teradyne TestStation ICT with Boundary Scan from GÖPEL electronic



- official verified System Integration of Teradyne and Göpel electronic
- reducing fixture costs with integrated System
- increased Test Access with combination of Boundary Scan and tester channels
- increased Test Coverage and detailed failure diagnosis
- reducing test points with Boundary Scan



Software Integration

- integrated program flow in GR228x/ Navigate-SW
- compact function call with CasconATE
- PASS/FAIL-flag setting and evaluation
- integrated error report of both systems
- flexible and time optimized SW-interface

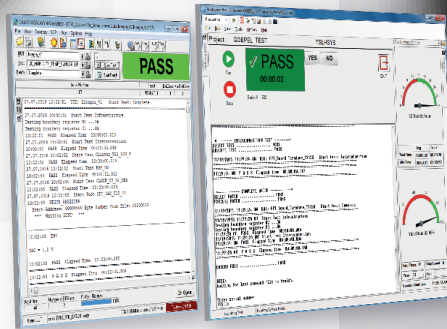
Application

- mid to high range production
- test and programming
- Mixed Signal Boards
- Embedded System Test
- Component Test
- Boundary Scan
- Panel Test

Hardware Integration

- fast and scalable Boundary Scan Controller
- industrial plug-in transceiver for long signal distances
- galvanic separated interface for Boundary Scan
- flexible adaption to special UUT interfaces
- stable tests with active delay compensation

TERADYNE
TESTSTATION



Key Features

- optimized wiring with decoupling of ICT and Boundary Scan
- manual interaction as application solution
- optional CION™ integration for interaction
- Reference board with demo project
- system diagnosis with SW-logfunction

Attributes

- compact, flexible hardware fitting inside ICT-slot
- seamless teamwork of both software suites
- global system and application support

Technical specifications

supported machine types	Teradyne TestStation TS12x, TSLH/LX, TSx51/52, GR228x
integrated Hardware	SFX/PCIe 1149, SFX-TAP4/228x-FXT (TAP8/PIC), TIC02/SR, SFX II CUBE
UUT interface (TAP, I/O, AUX etc.)	4/8 TAPs up to 80 Mhz; 32 PIP-Kanäle; 1.8 V up to 4.5 V; signal adaption; JTAG, BDM, SBW, SWD, PIC1x
automatic generated tests	Interconnection, RAM, Cluster, CPLD/FPGA/MCU-programming
technologies	Embedded JTAG Solutions: Boundary Scan, ChipVORX, VarioTAP, Embedded System Test

🇩🇪 Made in Germany



ISO 9001 certified

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