ATE System Integration radvne Spectrum ICT with Boundary Scan

Teradyne Spectrum ICT with Boundary Scan from GÖPEL electronic





- · official verified system integration of Teradyne and GÖPEL electronic
- · full automatic interaction of all ICT channels with Boundary Scan
- · increased test coverage and detailed failure diagnosis
- · increased test access with combination of Boundary Scan and tester channels
- · reducing test points with system integration





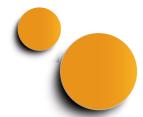
Software Integration

- · SYSTEM CASCON™ function integration into Spectrum software
- optimized interface for data transmission of both SW suites
- automatic test generator for interactive tests
- · PASS/FAIL flag setting and evaluation
- · integrated error report of both systems
- · usage of ICT channels as support for other tests
- · low test times with optimized SW interface

Application

- · mid to high range production
- test and programming
- · Mixed Signal Boards
- · Component Test
- · JTAG/Boundary Scan
- Panel Test

TERADYNE



Hardware Integration

- · fast and scalable Boundary Scan Controller
- · industrial plug-in transceiver for long signal distances
- flexible adaption to specia
 UUT interfaces
- · stable tests with active delay compensation
- · Special solution for externa CEPA Slot



Key Features

- · control of save PRISM connections
- optimized wiring with decoupling of ICT and Boundary Scan
- · interactive A/D or D/A converter test with usage of analogue functions
- · reference board with demo project
- sytem diagnosis with SW log function



Attributes

- compact, flexible hardward fitting inside ICT slot
- · seamless teamwork of both software suites
- global system and application support

Technical specifications

supported machine types	Teradyne Spectrum/88xx/TSSE
integrated Hardware	SFX/PCIe 1149, SFX-TAP4/88xx-FXT (TAP8/PIC), TIC02/SR
UUT-Interface (TAP,I/O,AUX etc.)	4/8 TAPs up to 80Mhz, 32 PIP-Channels, 1,8V to 4,5V, signal adaption; JTAG, BDM, SBW, SWD, PIC1x
automatic generated tests	Interconnection, RAM, Cluster, Interactive ATE, CPLD/FPGA/MCU/Flash-programming
technologies	Embedded JTAG Solutions: Boundary Scan, ChipVorx, VarioTAP

Made in Germany



GÖPEL electronic GmbH

Goeschwitzer Str. 58/60 07745 Jena/Thuringia 0049 3641 · 6896 0 Phone 0049 3641 · 6896 944 Fax

sales@goepel.com www.goepel.com







